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PTO/SB/17 (01-06)

Approved for use through 7/31/2006. OMB 0651-0032

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Fees pursuant to the Consolidated Appropriations Act, 2005 (H.R. 4818).

FEE TRANSMITTAL

For FY 2006

☐ Applicant claims small entity status. See 37 CFR 1.27

TOTAL AMOUNT OF PAYMENT (\$)

100.00

Complete if Known

| | |
|----------------------|-----------------------|
| Application Number | Patent#: 7,023,743 |
| Filing Date | Issued: April 4, 2006 |
| First Named Inventor | Hasan Nejad |
| Examiner Name | V. Q. Nguyen |
| Art Unit | 2827 |
| Attorney Docket No. | M4065.0509/P509 B |

METHOD OF PAYMENT (check all that apply)

☐ Check ☒ Credit Card ☐ Money Order ☐ None ☐ Other (please identify): _____
☐ Deposit Account Deposit Account Number: 04-1073 Deposit Account Name: Dickstein Shapiro LLP

For the above-identified deposit account, the Director is hereby authorized to: (check all that apply)

☐ Charge fee(s) indicated below ☐ Charge fee(s) indicated below, except for the filing fee
☒ Charge any additional fee(s) or underpayment of fee(s) under 37 CFR 1.16 and 1.17 ☒ Credit any overpayments

FEE CALCULATION (All the fees below are due upon filing or may be subject to a surcharge.)

1. BASIC FILING, SEARCH, AND EXAMINATION FEES

| Application Type | FILING FEES | | SEARCH FEES | | EXAMINATION FEES | | Fees Paid (\$) |
|------------------|-------------|-----------------------|-------------|-----------------------|------------------|-----------------------|----------------|
| | Fee (\$) | Small Entity Fee (\$) | Fee (\$) | Small Entity Fee (\$) | Fee (\$) | Small Entity Fee (\$) | |
| Utility | 300 | 150 | 500 | 250 | 200 | 100 | |
| Design | 200 | 100 | 100 | 50 | 130 | 65 | |
| Plant | 200 | 100 | 300 | 150 | 160 | 80 | |
| Reissue | 300 | 150 | 500 | 250 | 600 | 300 | |
| Provisional | 200 | 100 | 0 | 0 | 0 | 0 | |

2. EXCESS CLAIM FEES

| Fee Description | Fee (\$) | Small Entity Fee (\$) |
|--|----------|-----------------------|
| Each claim over 20 (including Reissues) | 50 | 25 |
| Each independent claim over 3 (including Reissues) | 200 | 100 |
| Multiple dependent claims | 360 | 180 |

Total Claims Extra Claims Fee (\$) Fee Paid (\$) Multiple Dependent Claims
 _____ - = _____ x _____ = _____
 Fee (\$) Fee Paid (\$)

HP = highest number of total claims paid for, if greater than 20.

Indep. Claims Extra Claims Fee (\$) Fee Paid (\$)

_____ - = _____ x _____ = _____

HP = highest number of independent claims paid for, if greater than 3.

3. APPLICATION SIZE FEE

If the specification and drawings exceed 100 sheets of paper (excluding electronically filed sequence or computer listings under 37 CFR 1.52(e)), the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).

Total Sheets Extra Sheets Number of each additional 50 or fraction thereof Fee (\$) Fee Paid (\$)

_____ - 100 = _____ /50 _____ (round up to a whole number) x _____ = _____

4. OTHER FEE(S)

Non-English Specification, \$130 fee (no small entity discount)

Other (e.g., late filing surcharge): 1811 Certificate of correction

100.00

SUBMITTED BY

| | | | | | |
|-------------------|-------------------|-----------------------------------|---------------|-----------|----------------|
| Signature | | Registration No. (Attorney/Agent) | 28,371 | Telephone | (202) 420-2232 |
| Name (Print/Type) | Thomas J. D'Amico | Date | July 24, 2006 | | |

Certificate

JUL 27 2006

of Correction

JUL 28 2006

JUL 28 2006



Docket No.: M4065.0509/P509 B
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:
Hasan Nejad et al.

Patent No.: 7,023,743

Issued: April 4, 2006

For: STACKED COLUMNAR 1T-NMTJ MRAM
STRUCTURE AND ITS METHOD OF
FORMATION AND OPERATION

**REQUEST FOR CERTIFICATE OF CORRECTION
PURSUANT TO 37 CFR 1.322 & 1.323**

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Upon reviewing the above-identified patent, Patentees noted typographical errors which should be corrected.

In the Specification, Applicants made the following errors to be corrected:

Column 1, line 48, "levels" should read --level--.

Column 2:

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Line 23, "2a" should read --2A--;

Line 26, "2b" should read --2B--;

Line 28, "2a" should read --2A--;

Line 29, "2c" should read --2C--;

Line 31, "2a" should read --2A--;

Line 32, "2d" should read --2D--;

Line 33, "2a" should read --2A--; and

Line 35, "2a-d" should read --2A-D--.

Column 3:

Line 5, "2a ... 2d" should read --2A ... 2D--;

Line 22, "2a, 2b, 2c and 2d" should read --2A, 2B, 2C and 2D--;

Line 29, "2a" should read --2A--; and

Line 36, "each access" should read --each of the access--.

Column 4:

Line 7, "2a" should read --2A--;

Line 14, "2a, 2b and 2c" should read --2A, 2B and 2C--;

Line 17, "2b" should read --2B--;

Line 29, "2d" should read --2D--; and

Line 36, "2a-d" should read --2A-D--.

Column 5, line 47, "Each access" should read --Each of the access--.

Column 6, line 34, "2a-c" should read --2A-C--.

Column 7:

Line 16, "one source" should read --one of the source--; and

Line 51, "include" should read --includes--.

Column 8, line 13, "via to the" should read --via the--.

In the Specification, the PTO made the following errors to be corrected:

Column 4:

Line 22, "ath" should read --ath--; and

Line 23, "ath" should read --ath--.

In the Claims, Applicants made the following error to be corrected:

Column 10, line 9, "associated said" should read --associated with said--.

In the Claims, the PTO made the following errors to be corrected:

Claim 1, column 9, line 2, "an associated transistor" should read --a single associated transistor--; and

Claim 5, column 10, line 5, "an associated transistor" should read --a single associated transistor--.

The Examiner's Amendment dated December 12, 2005 required Applicants to insert the word "single" after the word "an" and before the word "associated transistor" in claims 62 and 66. Those claims are mapped to claims 1 and 5.

The errors were found in the application as filed by Applicants and were made also by the PTO. Please charge our Credit Card in the amount of \$100.00 covering the fee set forth in 37 CFR 1.20(a). Credit Card Payment Form SB-2038, with a signature from an authorized cardholder, is enclosed.

The errors now sought to be corrected are inadvertent typographical errors the correction of which does not involve new matter or require reexamination.

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentees respectfully solicit the granting of the requested Certificate of Correction.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0509/P509 B.

Dated: July 24, 2006

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Michael A. Weinstein

Registration No.: 53,754

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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

Page 1 of 1

PATENT NO. : 7,023,743
APPLICATION NO. : 10/784,786
ISSUE DATE : April 4, 2006
INVENTORS : Hasan Nejad et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Specification, the following errors are corrected:

Column 1, line 48, "levels" should read --level--.

Column 2:

Line 23, "2a" should read --2A--;

Line 26, "2b" should read --2B--;

Line 28, "2a" should read --2A--;

Line 29, "2c" should read --2C--;

Line 31, "2a" should read --2A--;

Line 32, "2d" should read --2D--;

Line 33, "2a" should read --2A--; and

Line 35, "2a-d" should read --2A-D--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Thomas J. D'Amico
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JUL 28 2006

Column 3:

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Column 8, line 13, "via to the" should read --via the--.

In the Claims, the following errors are corrected:

Claim 1, column 9, line 2, "an associated transistor" should read --a single associated transistor--;

Claim 5, column 10, line 5, "an associated transistor" should read --a single associated transistor--; and

Claim 5, column 10, line 9, "associated said" should read --associated with said--.

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JUN 20 2007